

L Number	Hits	Search Text	DB	Time stamp
-	3410	((711/3,5,118-129,202).CCLS.)	USPAT; US-PGPUB	2002/05/06 11:43
-	538	((711/3,5,118-129,202).CCLS.) and instruction and (address adj (conver\$4 decode\$1 control\$2))) and SDRAM	USPAT; US-PGPUB	2002/05/06 12:36
-	1	((711/3,5,118-129,202).CCLS.) and instruction and (address adj (conver\$4 decode\$1 control\$2))) and (load adj module)	USPAT; US-PGPUB	2002/05/06 11:29
-	22	((711/3,5,118-129,202).CCLS.) and instruction and (address adj (conver\$4 decode\$1 control\$2))) and SDRAM	USPAT; US-PGPUB	2002/05/06 11:31
-	16	((711/3,5,118-129,202).CCLS.) and instruction and (address adj (conver\$4 decode\$1 control\$2))) and SDRAM) and @ad <=19991104	USPAT; US-PGPUB	2002/05/06 11:32
-	0	("l1 and ((dual multiple) adj memory adj management)").PN.	USPAT; US-PGPUB	2002/05/06 11:43
-	2	((711/3,5,118-129,202).CCLS.) and ((dual multiple) adj memory adj management)	USPAT; US-PGPUB	2002/05/06 11:47
-	38	((711/3,5,118-129,202).CCLS.) and ((dual multiple) with (memory adj management))	USPAT; US-PGPUB	2002/05/06 11:48
-	721	((711/3,5,118-129,202).CCLS.) and (instruction adj cache)	USPAT; US-PGPUB	2002/05/06 12:36
-	660	((711/3,5,118-129,202).CCLS.) and (instruction adj cache)) and @ad <= "19991104"	USPAT; US-PGPUB	2002/05/06 12:37
-	25	((711/3,5,118-129,202).CCLS.) and (instruction adj cache)) and @ad <= "19991104") and SDRAM	USPAT; US-PGPUB	2002/05/06 16:36
-	6838	(dual two many multi multiple plural plurality) with ((memory adj (control controller manager management)) (address adj ( convert\$2 conversion translate translator))))	USPAT; US-PGPUB	2002/05/06 16:43
-	6223	((dual two many multi multiple plural plurality) with ((memory adj (control controller manager management)) (address adj ( convert\$2 conversion translate translator)))) and @ad <= "19991104"	USPAT; US-PGPUB	2002/05/06 16:44
-	2671	((dual two many multi multiple plural plurality) with ((memory adj (control controller manager management)) (address adj ( convert\$2 conversion translate translator)))) and @ad <= "19991104") and instruction	USPAT; US-PGPUB	2002/05/06 16:44
-	102	((dual two many multi multiple plural plurality) with ((memory adj (control controller manager management)) (address adj ( convert\$2 conversion translate translator)))) and @ad <= "19991104") and instruction) and SDRAM	USPAT; US-PGPUB	2002/05/06 16:46
-	0	((dual two many multi multiple plural plurality) with ((memory adj (control controller manager management)) (address adj ( convert\$2 conversion translate translator)))) and @ad <= "19991104") and instruction) and SDRAM) and (logical adj address) and (phisical adj address)	USPAT; US-PGPUB	2002/05/06 16:47
-	14	((dual two many multi multiple plural plurality) with ((memory adj (control controller manager management)) (address adj ( convert\$2 conversion translate translator)))) and @ad <= "19991104") and instruction) and SDRAM) and (logical adj address)	USPAT; US-PGPUB	2002/05/06 16:47

-	676	(((dual two many multi multiple plural plurality) with ((memory adj (control controller manager management)) (address adj ( convert\$2 conversion translate translator)))) and @ad <= "19991104") and instruction) and DRAM	USPAT; US-PGPUB	2002/05/06 19:24
-	29	(((dual two many multi multiple plural plurality) adj ((memory adj (control controller manager management)) (address adj ( convert\$2 conversion translate translator)))) and @ad <= "19991104") and instruction) and DRAM	USPAT; US-PGPUB	2002/05/06 19:25